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MIT Conference , pp. 26-43, March, 1992. T RIPTYCH: An FPGA Architecture with Integrated Logic and Routing Scott Hauck, Gaetano Borriello, Carl Ebeling Department of Computer Science and Engineering University of Washington Seattle, WA 98195 Abstract It is composed of an integrated software system for mapping designs to a flexible, extensible hardware implementation platform. This allows ... Scott Hauck, Gaetano Borriello, Carl Ebeling Department of Computer Science and Engineering University of Washington Seattle, WA 98195. 2 compare them to the Springbok system. We then discuss